

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sunil Talwar et al.

Title: LOGIC CIRCUITS FOR PERFORMING THRESHOLD FUNCTIONS

Docket No.: 1365.065US1

Serial No.: 10/776,938

Filed: February 11, 2004

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2818

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

X A Communication Concerning Related Applications (2 pgs.).

X A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (2 pgs.), and copies of 15 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: 

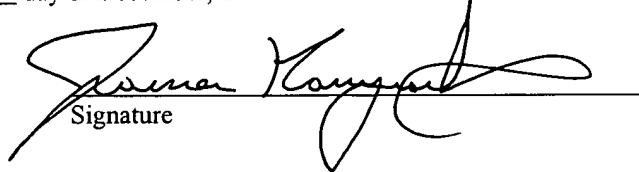
Atty: Timothy B Clise

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2nd day of December, 2004.



Name



Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

**S/N 10/776,938**

**PATENT**

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**COMMUNICATION CONCERNING RELATED APPLICATIONS**

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/637,532	August 11, 2000	1365.033US1	A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT
09/769,954	January 25, 2001	1365.039US1	A PARALLEL COUNTER AND A MULTIPLICATION LOGIC CIRCUIT
09/917,257	July 27, 2001	1365.051US1	A PARALLEL COUNTER AND A LOGIC CIRCUIT FOR PERFORMING MULTIPLICATION
10/472,658 2004/0103135 A1	September 22, 2003	1365.072US1	A MULTIPLICATION LOGIC CIRCUIT

10/714408      November      1365.063US1  
2004/0153490    14, 2003  
A1

LOGIC CIRCUIT AND METHOD FOR  
CARRY AND SUM GENERATION  
AND METHOD OF DESIGNING SUCH  
A LOGIC CIRCUIT

Respectfully submitted,

SUNIL TALWAR ET AL.

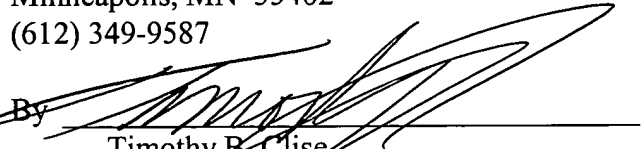
By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 349-9587

Date

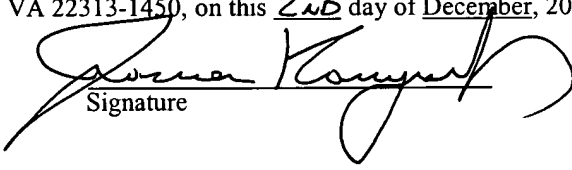
1 Dec '04

By

  
Timothy B. Clise  
Reg. No. 40,957

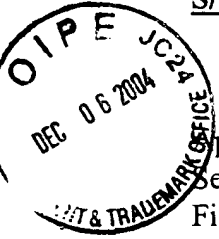
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JAMES KAU YUSIK  
Name

  
Signature

S/N 10/776,938

PATENT



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Sunil Talwar et al.	Examiner:	Unknown
Serial No.:	10/776,938	Group Art Unit:	2818
Filed:	February 11, 2004	Docket:	1365.065US1
Title:	LOGIC CIRCUITS FOR PERFORMING THRESHOLD FUNCTIONS		

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

The present application is a U.S. national patent application filed after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

SUNIL TALWAR ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 349-9587

Date

1 Dec '04

By



Timothy B Clise

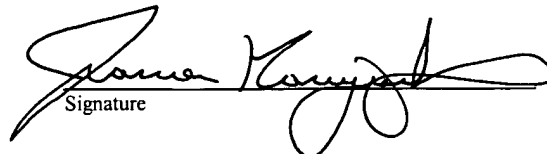
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Name

JAMES KAUYUSIK

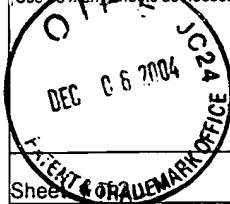
Signature



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

<b>Application Number</b>	10/776,938
<b>Filing Date</b>	February 11, 2004
<b>First Named Inventor</b>	Talwar, Sunil
<b>Group Art Unit</b>	2818
<b>Examiner Name</b>	Unknown

Sheet 1 of 1

Attorney Docket No: 1365.065US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2004/0103135 A1	05/27/2004	Talwar, S., et al.	09/22/2003
	US-2004/0153490 A1	08/05/2004	Talwar, S., et al.	11/14/2003
	US-5,701,504	12/23/1997	Timko, M. A.	12/28/1994

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
	GB-2263002	07/07/1993	Poon, J. T.	

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"PCT international Search Report relating to PCT/GB 03/05489", (September 15, 2004), 4 Pages	
		"United Kingdom Search Report relating to Great Britain Patent Application No. GB0326611.1", (March 29, 2004), 1 Page	
		BEDRIJ, O. J., "Carry-Select Adder", IRE Trans., EC-11, (June 1962), 340-346	
		KNOWLES, S., "A Family of Adders", Proc. 14th IEEE Symp. on Computer Arithmetic, (1999), 30-34	
		KOGGE, P. M., et al., "A Parallel Algorithm for the Efficient Solution of a General Class of Recurrence Equations", IEEE Trans. Computers, Vol. C-22, No. 8, (Aug. 1973), 786-793	
		LADNER, RICHARD E., et al., "Parallel Prefix Computation", Journal of ACM, Vol. 27, No. 4, (Oct. 1980), 831-838	
		LING, HUEY, "High-Speed Binary Adder", IBM Journal of Research and Development, Vol. 25, No. 3, (1981), 156-166	
		NICHOLSON, J. O., "Parallel-Carry Adders Listing Two-Bit Covers", IBM Technical Disclosure Bulletin, 22(11), (April, 1980), 5036-5037	
		ONG, S., et al., "A Comparison of ALU Structures for VLSI Technology", Proceedings, 6th Symposium on Computer Arithmetic (IEEE), (1983), 10-16	
		SCHMOOKLER, M. S., et al., "Group-Carry Generator", IBM Technical Disclosure Bulletin, 6(1), (June, 1963), 77-78	
		SKLANSKY, J., "Conditional-Sum Addition Logic", IRE Trans., EC-9, (June 1960), 226-231	
		WEINBERGER, A., et al., "A Logic for High-Speed Addition", Nat. Bur. Stand. Circ., 591, (1958), 3-12	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

<b>Application Number</b>	10/776,938
<b>Filing Date</b>	February 11, 2004
<b>First Named Inventor</b>	Talwar, Sunil
<b>Group Art Unit</b>	2818
<b>Examiner Name</b>	Unknown

Sheet 2 of 2

Attorney Docket No: 1365.065US1

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		WEINBERGER, A. , "Extension of the Size of Group Carry Signals", <u>IBM Technical Disclosure Bulletin</u> , 22(4), (September, 1979),1548-1550	
		WEINBERGER, A., "Improved Carry-Look-Ahead", <u>IBM Technical Disclosure Bulletin</u> , 21(6), (November, 1978),2460-2461	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

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